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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,002	11/26/2003	David Hoerl	105479-58451 (644-036)	7544
26345	7590	06/09/2009		
GIBBONS P.C. ONE GATEWAY CENTER NEWARK, NJ 07102			EXAMINER NGUYEN, THU HA T	
			ART UNIT 2453	PAPER NUMBER
			NOTIFICATION DATE 06/09/2009	DELIVERY MODE ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

IPDocket@gibbonslaw.com

Office Action Summary	Application No. 10/724,002	Applicant(s) HOERL, DAVID	
	Examiner THU HA T. NGUYEN	Art Unit 2453	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 20 February 2009.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 2-3 and 5-46 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 2, 3 and 5-46 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims **2-3, and 5-46** are presented for examination.

Response to Arguments

2. Applicant's arguments with respect to claims 2-3, 5-46 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 2-3, and 5-46 are rejected under 35 U.S.C. § 103 (a) as being unpatentable over **Comstock et al.** (hereinafter Comstock) US. Pub. No. **2004/0083266**, in view of **Chiang** U.S. Patent No. **6,271,822**.

5. As to claim 2, **Comstock** teaches the invention as claimed, including a system for improved video digitization and image correction, said system comprising:
a plurality of workstations coupled to a communications medium (figures 1-2);
a remote management unit coupled to said communications medium, said remote management unit including a digitizer converting analog video signals received from at least one of a plurality of remote networking devices to image correction

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processed digital video signals (figure 2, paragraphs 0030-0032, 0035-0034), the remote management unit including modules for processing and transmitting control signals to and from the plurality of workstations (figure 2, paragraphs 0012, 0035-0040) and transmitting the image correction processed digital signals to the plurality of workstations (figure 2, paragraph 0030-0033, 0035-0046).

Comstock teaches a digitizer converting analog video signals to digital video signals. However, **Comstock** does not explicitly teach a LCD controller converting analog video signals to digital video signals and wherein the LCD controller is a controller originally designed for driving a liquid crystal display.

Chiang teaches a LCD controller includes video digitizer that receives and converts analog signals into digital signals and wherein the LCD controller is a controller originally designed for driving a liquid crystal display (abstract, col. 3, line 65-col. 4, line 14, col. 5, lines 21-54).

It would have been obvious to one of ordinary skill in the Data Processing art at the time of the invention was made to modify the teachings of **Chiang** into **Comstock** system to include the LCD controller that includes video digitizer for converting analog-to-digital signals because it would provide an improvement in performance and fidelity system by using LCD driving circuit without the need to make many analog adjustments (see Chiang col. 6, lines 5-18).

6. As to claim 3, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein each of said plurality of workstations is of a type

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comprising at least one keyboard, video monitor and cursor control device, and wherein each of said plurality of workstations is capable of accessing and operating said plurality of remote networking devices through said remote management unit (figures 1-2).

7. As to claim 5, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein said communications medium is at least one selected from the group consisting of a LAN, a WAN, a wireless connection, a modem, a direct modem connection, and the Internet (paragraphs 0020-0021).

8. As to claim 6, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein each of said plurality of remote networking devices is connected to said remote management unit through cabling via a port selected from the group consisting of a serial port, parallel port, keyboard port, video port, cursor control device port, USB port, firewire port, bluetooth port, Ethernet port, and a power supply port (figures 1-2, paragraphs 0020-0021).

9. As to claim 7, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein said remote management unit controls access by requiring identification data to authenticate a user (paragraph 0027).

10. As to claim 8, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein said remote management unit and said plurality of user workstations communicate via TCP/IP (paragraphs 0020-0023).

11. As to claim 9, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein said remote management unit and said plurality of user workstations communicate via the Internet (figures 1-2, paragraphs 0020-0021).

12. As to claim 10, **Chiang** teaches the invention as claimed, including the system according to claim 2, wherein said LCD controller includes an analog to digital converter (figure 2, abstract).

13. As to claim 11, **Chiang** teaches wherein said LCD controller includes an input interface circuit for detecting a color palette utilized by said remote network device (col. 2, lines 2-43).

14. As to claim 12, **Chiang** teaches wherein said LCD controller includes a synchronization selector circuit for receiving horizontal and vertical synchronization signals (col. 1, line 24-col. 2, line 67, col. 3, line 50-col. 5, line 9).

15. As to claim 13, **Chiang** teaches wherein said LCD controller includes a mode detection circuit for receiving said synchronization signals from said

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synchronization selector circuit and for determining a frequency of said synchronization signals (col. 1, line 24-col. 2, line 67, col. 3, line 50-col. 5, line 9).

16. As to claim 14, **Chiang** teaches wherein said LCD controller includes an auto-adjustment circuit for performing at least one of active area detection, pixel brightness searching, pixel measurement and phase distortion measurement (col. 6, line 28-col. 8, line 42).

17. As to claim 15, **Chiang** teaches wherein said auto-adjustment circuit updates timing of a clock during said phase distortion measurement (col. 1, line 24-48, col. 2, line 44-58).

18. As to claim 16, **Chiang** teaches wherein said LCD controller includes a downscaler circuit for reducing high video resolution to low video resolution (col. 5, line 10-47).

19. As to claim 17, **Chiang** teaches wherein said LCD controller includes an upscaler circuit for increasing low video resolution to high video resolution (col. 5, line 10-47).

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20. As to claim 18, **Chiang** teaches wherein said LCD controller includes an option menu circuit for enabling a user to select one of a plurality of serial devices, remote servers, remote computers or power devices (figure 1).

21. As to claim 19, **Chiang** teaches wherein said LCD controller modifies each pixel of said digital video signals according to a color palette (col. 2, lines 2-43).

22. As to claim 20, **Chiang** teaches wherein said LCD controller includes a dithering circuit for approximating a color for a pixel of said digital video signals (col. 2, lines 2-43).

23. As to claim 21, **Chiang** teaches wherein said LCD controller includes an output interface circuit for adjusting timing of said analog video signals (col. 2, lines 2-43).

24. As to claim 22, **Comstock** teaches the invention as claimed in claim 2, wherein said remote management unit includes a video processor circuit for compressing said digital video signals (paragraphs 0038, 0065).

25. As to claim 23, **Chiang** teaches wherein said video processor circuit includes a pixel receiving circuit for receiving pixel information from said digital video signals (abstract, col. 3, line 65-col. 4, line 14, col. 5, lines 21-54).

26. As to claim 24, **Chiang** teaches wherein said video processor circuit includes a frame buffer circuit for storing said pixel information (col. 1, line 58-57).

27. As to claim 25, **Comstock** teaches the invention as claimed, including the system according to claim 22, wherein said video processor circuit includes a video compression circuit (paragraphs 0038, 0065).

28. As to claim 26, **Comstock** teaches the invention as claimed, including the system according to claim 2, wherein said processing includes converting said digital video signals for compatibility with a video display of one of said plurality of workstations (figure 2, paragraphs 0030-0032, 0035-0034). **Comstock** does not explicitly teach a LCD controller.

Chiang teaches a LCD controller includes video digitizer that receives and converts analog signals into digital signals (abstract, col. 3, line 65-col. 4, line 14, col. 5, lines 21-54).

29. It would have been obvious to one of ordinary skill in the Data Processing art at the time of the invention was made to modify the teachings of **Chiang** into **Comstock** system to include the LCD controller that includes video digitizer for converting analog-to-digital signals because it would provide an improvement in performance and fidelity system by using LCD driving circuit without the need to make many analog adjustments (see Chiang col. 6, lines 5-18).

30. As to claim 27, **Comstock** teaches the invention as claimed, including a method for providing improved video digitization and image correction for the transmission of video signals, said method comprising the steps of:

receiving analog video signals and control signals from one of a plurality of remote devices connected to a remote management unit (figures 1-2, paragraphs 0012, 0034-0040);

using digitizer to converts said analog video signals to digital video signals (figure 2, paragraphs 0030-0032, 0035-0034) and corrects said digital video signals (figure 2, paragraphs 0030-0032, 0035-0034, 0043); and

transmitting said digital video signals and the control signals to one of a plurality of user interface devices (figures 1-2, paragraphs 0035-0046).

Comstock teaches a digitizer converting analog video signals to digital video signals. However, **Comstock** does not explicitly teach a LCD controller converting analog video signals to digital video signals and wherein the LCD controller is a controller originally designed for driving a liquid crystal display.

Chiang teaches a LCD controller includes video digitizer that receives and converts analog signals into digital signals and wherein the LCD controller is a controller originally designed for driving a liquid crystal display (abstract, col. 3, line 65-col. 4, line 14, col. 5, lines 21-54).

It would have been obvious to one of ordinary skill in the Data Processing art at the time of the invention was made to modify the teachings of **Chiang** into **Comstock**

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system to include the LCD controller that includes video digitizer for converting analog-to-digital signals because it would provide an improvement in performance and fidelity system by using LCD driving circuit without the need to make many analog adjustments (see Chiang col. 6, lines 5-18).

31. As to claim 28, **Comstock** teaches the invention as claimed, including the method according to claim 27, wherein said user interface devices are accessible by inputting unique authentication information (paragraph 0027).

32. As to claim 29, **Comstock** teaches the invention as claimed, including the method according to claim 27, wherein said method further comprises the step of: displaying said digital video signals on a video display of one of said user interface devices (figure 2, paragraphs 0043-0046).

33. As to claim 30, **Comstock** teaches the invention as claimed, including the method according to claim 27, wherein said method further comprises the step of: compressing said digital video signals prior to said transmitting (paragraphs 0038, 0065).

34. As to claim 31, **Comstock** teaches the invention as claimed, including the method according to claim 30, wherein a compression algorithm is used to perform said compressing (paragraphs 0038, 0065).

35. As to claim 32, **Chiang** teaches wherein said compression algorithm determines noise in said digital video signals, smoothes said digital video signals, determines changes to pixels of said digital video signals, and compresses said changed digital video signals (col. 1, line 24-col. 2, line 67, col. 3, line 50-col. 5, line 9).

36. As to claim 33, **Comstock** teaches the invention as claimed, including the method according to claim 27, wherein said transmitting occurs via TCP/IP (paragraphs 0020-0022).

37. As to claim 34, **Chiang** teaches wherein said correcting comprises image correction (col. 1, line 24-col. 2, line 67, col. 3, line 50-col. 5, line 9).

38. As to claim 35, **Chiang** teaches wherein said image correction includes detecting a color palette of said digital video signals (col. 2, lines 2-43).

39. As to claim 36, **Chiang** teaches wherein said correcting includes receiving horizontal and vertical synchronization signals (col. 1, line 24-col. 2, line 67, col. 3, line 50-col. 5, line 9).

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40. As to claim 37, **Chiang** teaches wherein said correcting includes determining one or more frequencies of said digital video signals (col. 1, line 24-col. 2, line 67, col. 3, line 50-col. 5, line 9).

41. As to claim 38, **Chiang** teaches wherein said correcting includes detecting an active area of a video image represented by said digital video signals (col. 9, line 50-col. 11, line 20, col. 15, line 66-col. 17, line 37).

42. As to claim 39, **Chiang** teaches wherein said correcting includes determining brightness of each pixel of said digital video signals (col. 6, line 28-col. 8, line 42).

43. As to claim 40, **Chiang** teaches wherein said correcting includes measuring phase distortion of said digital video signals (col. 1, line 24-48, col. 2, line 44-58).

44. As to claim 41, **Chiang** teaches wherein said correcting includes measuring one or more pixels of said digital video signals (col. 1, line 24-48, col. 2, line 44-58).

45. As to claim 42, **Chiang** teaches wherein said correcting includes reducing high video resolution to low video resolution (col. 5, line 10-47).

46. As to claim 43, **Chiang** teaches wherein said correcting includes increasing low video resolution to high video resolution (col. 9, line 50-col. 11, line 20, col. 15, line 66-col. 17, line 37).

47. As to claim 44, **Chiang** teaches wherein said correcting includes dithering said digital video signals (col. 2, lines 2-43).

48. As to claim 45, **Chiang** teaches wherein said correcting includes adjusting timing of said digital video signals (col. 2, lines 2-43).

49. As to claim 46, **Chiang** teaches wherein said method further comprises the step of: storing pixel information of digital video signals (col. 1, line 58-57).

Conclusion

50. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the

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shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

51. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thu Ha Nguyen, whose telephone number is (571) 272-3989. The examiner can normally be reached Monday through Friday from 8:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ario Etienne, can be reached at (571) 272-4001.

The fax phone numbers for the organization where this application or proceeding is assigned are (571) 273-8300 for regular communications.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/THUHA T. NGUYEN/

Primary Examiner, Art Unit 2453

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